

*ABSTRACT AMENDMENTS*

Replace the Abstract with:

~~The present invention is provided with An apparatus for controlling a power converter including a voltage-vector control unit (11) that determines, based on voltage instruction values  $V_u$ ,  $V_v$ , and  $V_w$ , value for the power converter, a voltage vectors vector output from a power converter in one control cycle of PWM pulse width modulation control and times for outputting of the voltage vectors vector, a voltage-vector adjusting unit (12) that adjusts output times of the time of outputting of the voltage vectors input from the voltage vector control unit (11) vector so that time of outputting of a zero-voltage vector is larger than a fixed time or zero, and a firing-pulse generating unit (13) that generates, based on the output times of the voltage vectors adjusted by the voltage vector adjusting unit, a signal for turning either of on and off a semiconductor switching elements forming element included in the power converter, based on the time of outputting of the voltage vector adjusted by the voltage-vector adjusting unit. The voltage vector adjusting unit (12) adjusts a zero voltage-vector output time so as to ensure that the output time is equal to or larger than a predetermined value. With this, a high voltage exceeding twice a direct current bus voltage can be suppressed. Three phases can be collectively controlled.~~